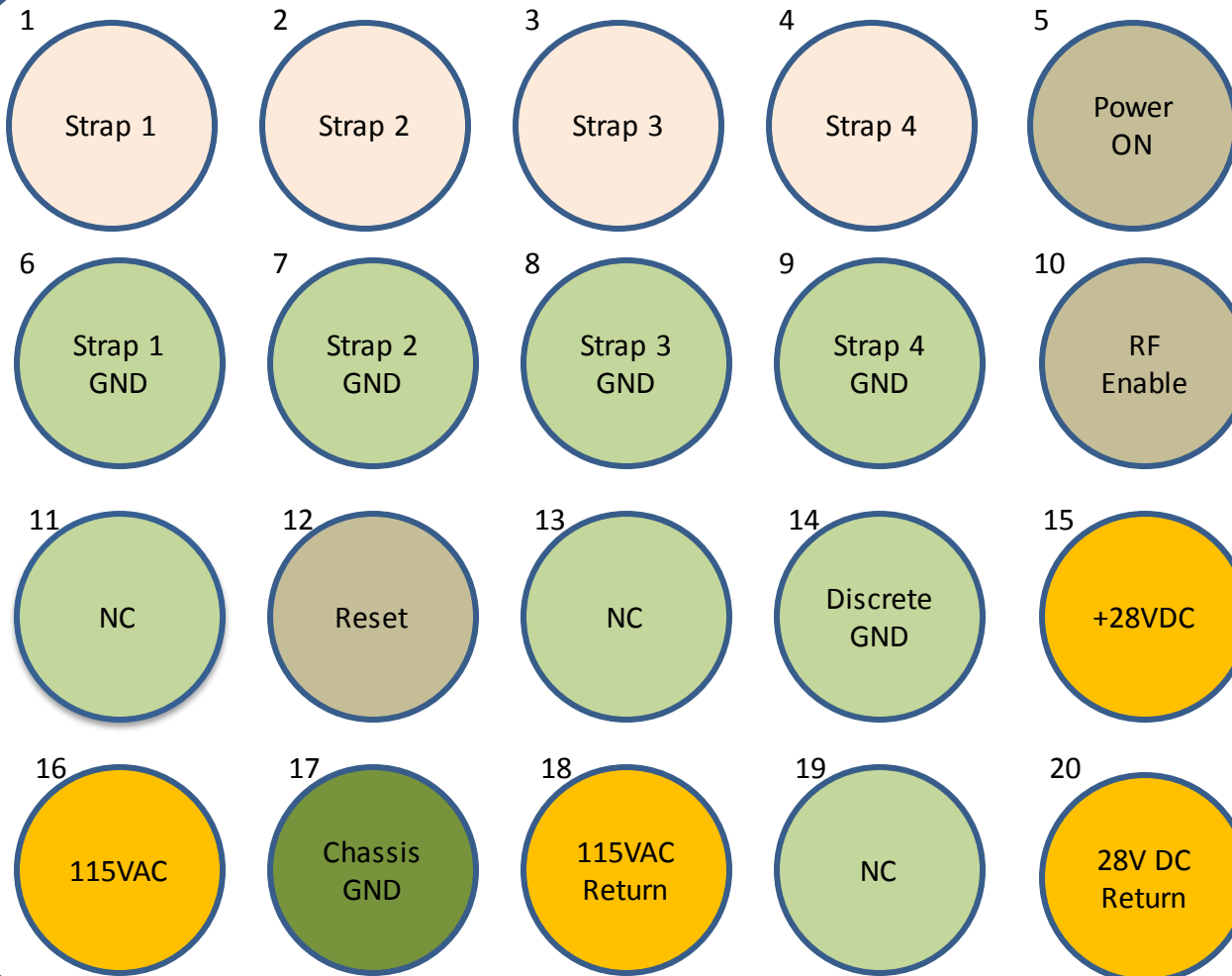


Proposed CWAP Pinout Changes

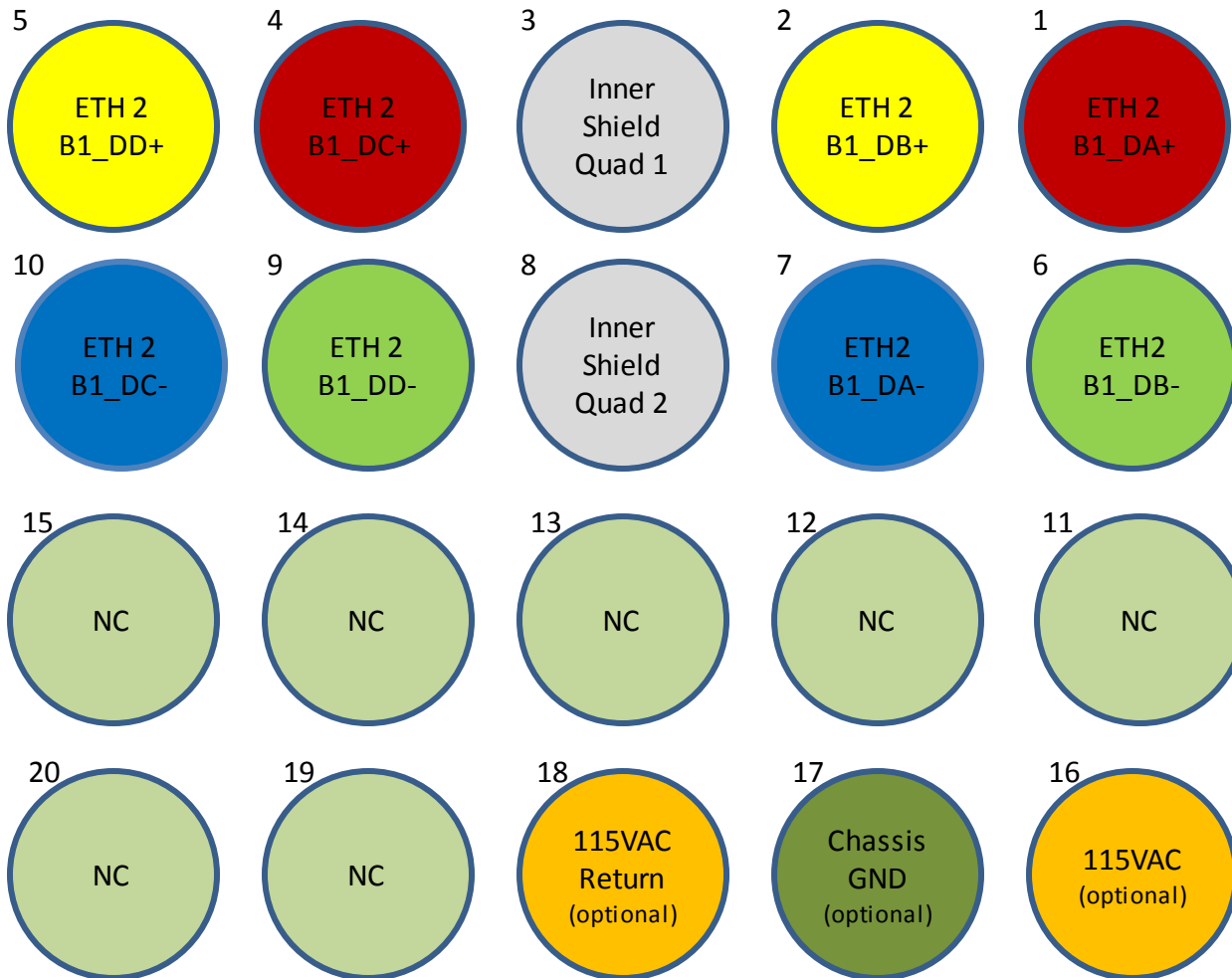
- Pin outs must be optimized because:
 - EN4165 w/ standard 20-22 insert is not designed for high speed data and power inner mixed
 - Reduce EMI/EMC Environmental risks with a connector having unshielded power and I/O and un-shielded, un-twisted Ethernet pairs
 - Specific changes are as follows and should not affect daisy chaining capabilities
 - Swap Ethernet 1 and IP straps
 - Full line of ground pins between power and I/O to eliminate EMI
 - Remove daisy chaining Discrete Lines.
 - EMI antennas these are EMI antennae inside the connector
 - Reliability concerns further away from signal generator
 - No control of DOM
 - Signals can still be routed with the cabling and have a separate connector that connects the lines outside the CWAP and the DOM for daisy chaining.
 - Daisy Chain power is optional and still reserved
 - J3 pins 11-20 where vendor specific however it was requested they become an optional third Ethernet port

CWAP - J1(pins)



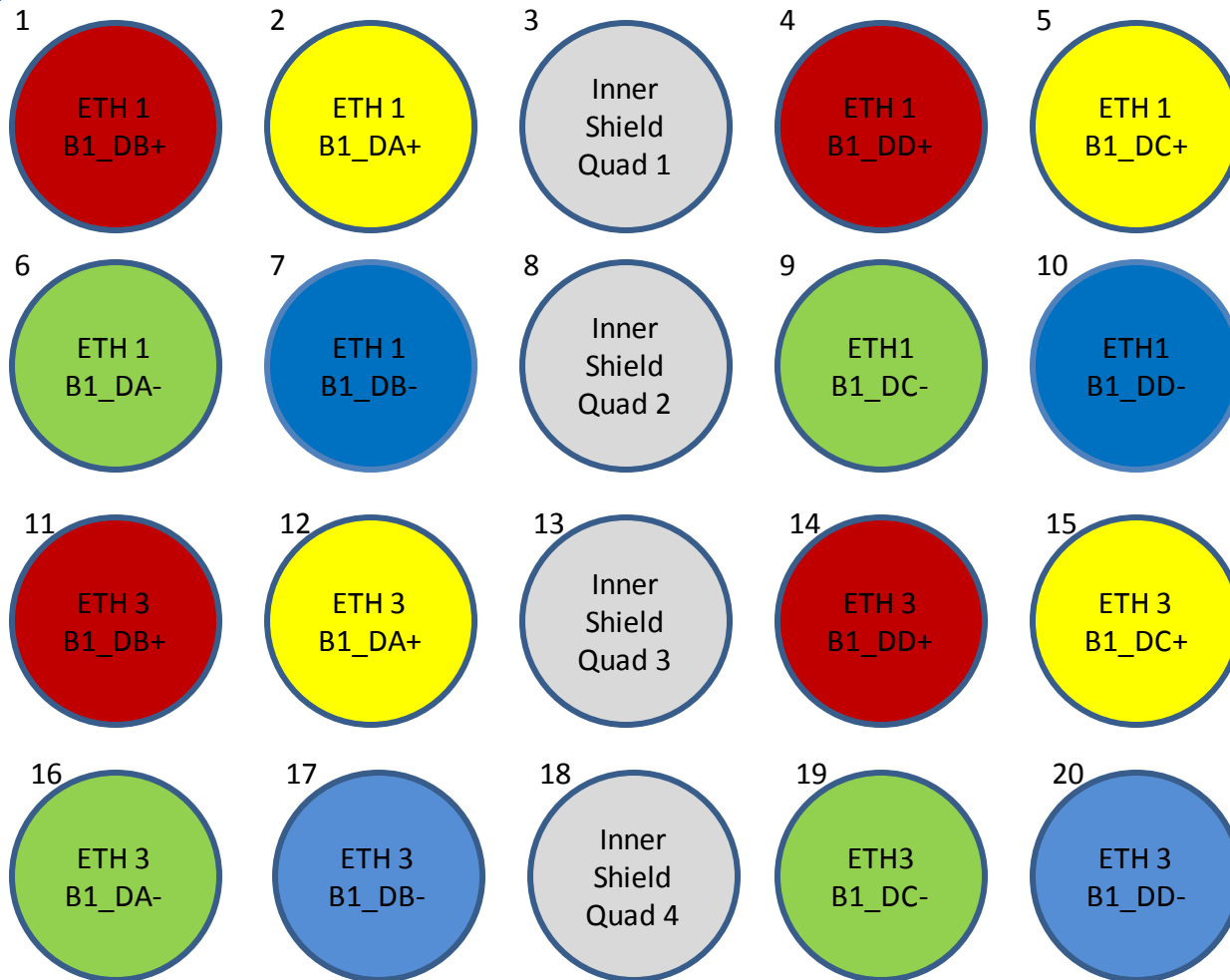
PIN ID	Signal Name	Description
1	Strap 1	IP_STRAP_0
2	Strap 2	IP_STRAP_1
3	Strap 3	IP_STRAP_2
4	Strap 4	IP_STRAP_3
5	PWR ON	GND = PWR ON OPEN=PWR OFF
6	Strap 1 GND	Signal GND
7	Strap 2 GND	Signal GND
8	Strap 3 GND	Signal GND
9	Strap 4 GND	Signal GND
10	RF Enable	GND = RF ON OPEN=RF OFF
11	NC	NC in plane
12	Reset	NC in plane
13	NC	NC in plane
14	Discrete GND	Signal GND
15	+28VDC	+ DC PWR
16	115 VAC	AC Pwr 5Amax
17	Chassis GND	Chassis
18	115 VAC RTN	AC Pwr RTN
19	NC	NC in plane
20	28 VDC Return	DC PWR GND

CWAP – J2 (sockets)



PIN ID	Signal Name	Description
1	B1_DA+	ETH2
2	B1_DB+	ETH2
3	Shield 1	Chassis
4	B1_DC+	ETH2
5	B1_DD+	ETH2
6	B1_DB-	ETH2
7	B1_DA-	ETH2
8	Shield 2	Chassis
9	B1_DD-	ETH2
10	B1_DC-	ETH2
11	NC	No Connect
12	NC	No Connect
13	NC	No Connect
14	NC	No Connect
15	NC	No Connect
16	115 VAC optional	Not Supported
17	Chassis GND optional	No connect in plane
18	115VAC RTN optional	Not Supported
19	NC	No Connect
20	NC	No Connect

CWAP – J3(pins)



1	ETH 1 B1_DB+	ETH1
2	ETH 1 B1_DA+	ETH1
3	Shld GND1	ETH1
4	ETH 1 B1_DD+	Chassis
5	ETH 1 B1_DC+	ETH1
6	ETH 1 B1_DA-	ETH1
7	ETH 1 B1_DB-	ETH1
8	Shld GND2	Chassis
9	ETH1 B1_DC-	ETH1
10	ETH1 B1_DD-	ETH1
11	ETH 3 B1_DB+	ETH3
12	ETH 3 B1_DA+	ETH3
13	Inner Shield Quad 3	Chassis
14	ETH 3 B1_DD+	ETH3
15	ETH 3 B1_DC+	ETH3
16	ETH 3 B1_DA-	ETH3
17	ETH 3 B1_DB-	ETH3
18	Inner Shield	Chassis